

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	2	"6075935".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/24 09:05
L4	0	@ad<"20020709" and configurable adj I/O and pins near external adj I/O adj device	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/24 09:07
L5	107	@ad<"20020709" and configurable adj I/O and pins	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/24 09:19
L6	0	L5 and input adj value near external near device	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/24 09:08
L7	4	L5 and external near input	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/24 09:08
L8	0	@ad<"20020709" and configurable adj I/O and pins and dependent near pin near external	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/24 09:20
L9	0	@ad<"20020709" and configurable adj I/O and pins and dependent near pin near input	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/24 09:20
L10	0	@ad<"20020709" and configurable adj I/O and dependent near pin near input	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/24 09:20
L11	0	@ad<"20020709" and configurable adj I/O adj pin and external near input	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/24 09:21

L12	0	@ad<"20020709" and configurable adj I/O adj pin and external adj input	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/24 09:21
L13	0	@ad<"20020709" and configurable adj I/O adj pin and input near device	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/24 09:21
L14	0	@ad<"20020709" and adjustable adj I/O adj pin and input near device	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/24 09:21
L15	0	@ad<"20020709" and adjustable adj I/O adj pin	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/24 09:21
L16	8	@ad<"20020709" and configurable near I/O adj pin	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/24 09:40
L17	11	@ad<"20020709" and configurable near I/O near pins	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/24 09:40
S6	49	S5 and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 08:44
S36	0	@ad<"20020709" and initialize adj CPLD and ordinary adj skill	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 08:44
S37	0	@ad<"20020709" and initialize adj CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 08:45
S38	7	@ad<"20020709" and initialize adj PLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/24 09:07

S39	1	@ad<"20020709" and initialize near CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 08:51
S40	0	@ad<"20020709" and initialize near erase near CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 09:04
S41	0	@ad<"20020709" and erase near CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 09:05
S42	1	@ad<"20020709" and erase near programmable adj logic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 09:06
S43	0	@ad<"20020709" and erase near programmable adj logic and CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 09:06
S44	3	@ad<"20020709" and erase near PLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 09:07
S45	0	@ad<"20020709" and initialize near erase near CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 09:08
S46	0	@ad<"20020709" and format near CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:32
S47	366	@ad<"20020709" and protocol and program\$3 near chip	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:29
S48	0	S47 and select near protocol	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:33

S49	2	S47 and configurable near I/O	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:33
S50	2	S49 and pins	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:33
S51	2	S50 and controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:33
S52	160	@ad<"20020709" and select same protocol and config\$6 same pins	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:37
S53	21	@ad<"20020709" and select same protocol same config\$6 same pins	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:42
S54	19	S53 and program\$4 and chip	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:38
S55	15	S53 and program\$4 and IC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:38
S56	0	S53 and program\$4 near chip	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:38
S57	0	S53 and program\$4 near IC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:39
S58	5	S53 and program\$4 same chip	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:39

S59	5	S53 and program\$4 same IC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:39
S60	0	switch adj matrix and ASIC and @ad<"20020709" and hot adj plugging	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S61	2	switch adj matrix near3 ASIC and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S62	0	universal near3 configurable adj chip and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S63	0	universal near3 configurable adj chip	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S64	69	universal near3 ASIC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S65	49	S64 and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S66	0	S65 and configurable adj ASIC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S67	29	configurable adj ASIC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S68	26	S67 and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55

S69	0	S68 and switch adj matrix	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S70	0	S68 and generic adj chip	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S71	37	S65 and switch	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S72	0	S71 and hot adj plug	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S73	9	S71 and eeprom	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S74	55	ASIC adj switch and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S75	1396	S74 programmable adj switch	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S76	0	S74 and programmable adj switch	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S77	1140	programmable adj switch and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S78	0	programmable adj I?O adj switch and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55

S79	12	programmable adj switch near4 I?O and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S80	8879	eeeprom and initialization	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S81	0	eeeprom near program adj FPGA and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S82	0	eeeprom near (program adj FPGA) and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S83	0	eeeprom near (initialization adj FPGA) and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S84	20	(initialization adj FPGA) and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S85	4	S84 and EEPROM	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S86	0	FPGA adj load near eeeprom and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S87	2	"6181159".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S88	0	EEPROM adj initialization near FPGA and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55

S89	0	ROM adj initialization near FPGA and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S90	27	initialization near FPGA and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S91	8	S90 and eeprom	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S92	10	710/2,9-14,62,72.ccls. and switch adj programmable and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S93	0	@ad<"20020709" and initialize adj CPLD and ordinary adj skill	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S94	0	@ad<"20020709" and initialize adj CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S95	7	@ad<"20020709" and initialize adj PLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S96	1	@ad<"20020709" and initialize near CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S97	0	@ad<"20020709" and initialize near erase near CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S98	0	@ad<"20020709" and erase near CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55

S99	0	@ad<"20020709" and erase near programmable adj logic and CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S10 0	1	@ad<"20020709" and erase near programmable adj logic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S10 1	3	@ad<"20020709" and erase near PLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S10 2	0	@ad<"20020709" and initialize near erase near CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S10 3	0	@ad<"20020709" and format near CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 11:55
S10 4	0	@ad<"20020709" and initialize adj CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:07
S10 5	0	@ad<"20020709" and initialize adj I/O adj devices and CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:08
S10 6	12	@ad<"20020709" and initialize adj I/O adj devices	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:12
S10 7	0	@ad<"20020709" and initialize adj I/O adj devices and config\$6 adj chip	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:08
S10 8	50	@ad<"20020709" and initial adj state and CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:16

S10 9	0	@ad<"20020709" and initial adj state near I/O adj device and CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:16
S11 1	0	@ad<"20020709" and initialize adj devices and CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:22
S11 2	4	@ad<"20020709" and initialize adj FPGA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:30
S11 3	0	@ad<"20020709" and reset adj CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:30
S11 4	3	@ad<"20020709" and reset near CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:30
S11 5	1	@ad<"20020709" and initialize near CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:31
S11 6	0	@ad<"20020709" and reprogram adj CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:45
S11 7	0	@ad<"20020709" and clear adj CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:45
S11 8	0	@ad<"20020709" and erase adj CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:45
S11 9	0	@ad<"20020709" and reprogrammable adj CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:48

S12 0	2	"6181159".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:46
S12 1	9	@ad<"20020709" and reprogrammable adj array	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:56
S12 2	0	@ad<"20020709" and program same initialize same block same CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:57
S12 3	0	@ad<"20020709" and initialize same block same CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:57
S12 4	11	@ad<"20020709" and initialize same logic same CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 12:59
S12 5	0	@ad<"20020709" and format adj CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 13:00
S12 6	1	@ad<"20020709" and initial adj state near CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 13:02
S12 7	0	@ad<"20020709" and logic adj blocks same initialize same CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 13:02
S12 8	26	@ad<"20020709" and logic adj blocks same initialize	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 13:02
S12 9	2	@ad<"20020709" and logic adj blocks same initialize and FPGA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 13:13

S13 0	0	@ad<"20020709" and logic adj blocks same initialize and CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 13:02
S13 1	1	@ad<"20020709" and logic adj blocks same clear and CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 13:56
S13 2	1512	@ad<"20020709" and altera.asn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 13:56
S13 3	83	@ad<"20020709" and altera.asn. and CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 13:56
S13 4	1	S133 and reprogram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 13:57
S13 5	0	S133 and initialize near CPLD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 13:59
S13 6	3625	326/37-50.ccls. and @ad<"20020709"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 13:59
S13 7	2	S136 and CPLD and initialize	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/24 09:04